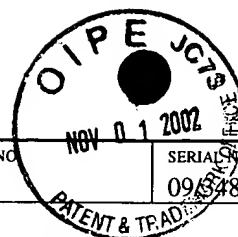


FORM PTO-1449		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		ATTORNEY DOCKET NO. 5231.4-4002		SERIAL NO. 693,483,317	
INFORMATION DISCLOSURE CITATION (Use several sheets if necessary)				APPLICANT John S. Yates Jr. et al.		RECEIVED NOV 04 2002 Technology Center 2100	
				FILING DATE July 7, 1999			
U.S. PATENT DOCUMENTS							
EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE	
CD	3,544,969	12/01/1970	Rakoczi	340	172.5	Nov. 27, 1967	
CD	3,781,823	12/25/1973	Senese	340	172.5	July 28, 1972	
CD	4,084,235	04/11/1978	Hirtle	364	200	Apr. 14, 1975	
CD	4,455,602	06/19/1984	Baxter	364	200	May 22, 1981	
CD	4,514,803	4/30/1985	Agnew	364	200	Apr. 26, 1982	
CD	4,779,187	10/18/1988	Letwin	364	200	Apr. 10, 1985	
CD	4,812,975	3/14/1989	Adachi	364	300	Jul. 11, 1984	
CD	5,115,500	5/19/1992	Larsen	395	425	Jan. 11, 1988	
CD	5,335,331	8/2/1994	Murao	395	375	Jul. 12, 1991	
CD	5,481,684	2/2/1996	Richter	395	375	Jul. 20, 1994	
CD	5,481,693	01/02/1996	Blomgren	395	375	July 20, 1994	
CD	5,542,059	07/30/1996	Blomgren	395	375	Dec. 21, 1994	
CD	5,542,109	07/30/1996	Blomgren	395	800	Aug. 31, 1994	
CD	5,568,646	10/22/1996	Jaggard	395	800	Sep. 19, 1994	
CD	5,619,666	04/08/1997	Coon	395	384	June 2, 1995	
CD	5,638,525	06/10/1997	Hammond	395	385	Feb. 10, 1995	
CD	5,678,032	10/14/1997	Woods	395	500	Sep. 6, 1995	
CD	5,751,982	5/12/1998	Morley	395	800	Mar. 31, 1995	
CD	5,781,750	7/14/1998	Blomgren	395	385	Jan. 11, 1994	
CD	5,822,578	10/13/1998	Frank	395	591	Jun. 5, 1995	
CD	5,832,205	11/03/1998	Kelly	395	185.06	Aug. 20, 1996	
CD	5,842,017	11/24/1998	Hookway	395	707	Jan. 29, 1996	
CD	5,920,721	07/06/1999	Hunter	395	705	Jun. 11, 1997	
CD	5,930,509	07/27/1999	Yates	395	707	Jan. 29, 1996	
CD	5,958,047	09/28/1999	Panwar	712	237	Jun. 25, 1997	
CD	6,000,028	12/07/1999	Chernoff	712	226	Jan. 29, 1996	
CD	6,006,029	12/21/1999	Bianchi	395	500.45	Sep. 28, 1993	
CD	6,021,265	02/01/2000	Nevill	395	385	Apr. 14, 1997	
CD	6,031,992	02/29/2000	Cmelik	395	705	Jul. 5, 1996	
CD	6,091,897	07/18/2000	Yates	395	708	Jan. 29, 1996	
CD	6,163,764	12/19/2000	Dulong	703	26	Oct. 12, 1998	
CD	6,219,774	4/17/2001	Hammond	711	202	Mar. 25, 1998	
EXAMINER <i>Charles C. [Signature]</i>				DATE CONSIDERED <i>10/4/03</i>			

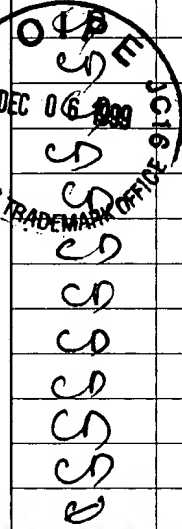


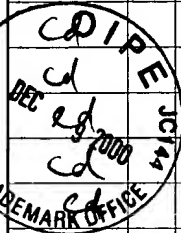
FORM PTO-1449		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		ATTORNEY DOCKET NO. 5231.4-4002		SERIAL NO. 09/348,317	
INFORMATION DISCLOSURE CITATION (Use several sheets if necessary)				APPLICANT John S. Yates Jr., et al.		GROUP ART UNIT 2784	
				FILING DATE July 7, 1999			
FOREIGN PATENT DOCUMENTS							
		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION YES NO
		WO 96/24895	08/15/96	PCT/US	G06F	9/22	NOV 04 2002
RECEIVED Technology Center 2100							
OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Papers, Etc.)							
CD		Burroughs, Preliminary Edition, B 1700 Systems Reference Manual, Burroughs Corporation (1972)					
CD		Burroughs, B 1700 Systems Micro Implementation Language (MIL) Reference Manual, Burroughs Corporation (1973)					
CD		Anton Chernoff, et al., FX!32, A Profile-Directed Binary Translator, IEEE Micro, Vol. 18 No. 2 pp. 56-64 (March/April, 1998)					
CD		Compaq Computer Corp., Compiler Writer's Guide for the Alpha 21264, 1999					
CD		Digital Equipment Corp., White Paper: How DIGITAL FX!32 Works (September 1997) http://www.digital.com/semiconductor/amt/fx32/fx-white.htm					
CD		Ebcioglu and Altman, IBM Research Report, DAISY: Dynamic Compilation for 100% Architectural Compatibility, IBM Research Division (1996)					
CD		Ronald M. Guffin, Microdiagnostics for the Standard Computer MLP-900 Processor, IEEE Transactions on Computers, Vol. C-20 No. 7, pp. 803-808 (July 1971)					
CD		Linley Gwennap, MDR Technical Library Special Report, Intel's Merced and IA-64 Technology and Market Forecast, IA-64 Software Model - Chapter 3 excerpt Microprocessor Report, MicroDesign Resources (July 20, 1998)					
CD		Linley Gwennap, MDR Technical Library Special Report, Intel's Merced and IA-64: Technology and Market Forecast, Executive Summary, Microprocessor Report, MicroDesign Resources (July 20, 1998)					
CD		Linley Gwennap, First Merced Patent Surfaces, Intel Document Reveals Processors With Dual Instruction Sets, Microprocessor Report, MicroDesign Resources (July 20, 1998)					
CD		Raymond J. Hookway, et al., Combining Emulation and Binary Translation (August 28, 1997) http://www.digital.com/info/DTJP01HM.HTM					
CD		Intel Corporation, Intel Architecture Software Developer's Manual, passim (1997)					
CD		Harold W. Lawson, Jr., et al., Functional Characteristics of a Multilingual Processor, IEEE Transactions on Computers, Vol. C-20, No. 7, pp. 732-742 (July, 1971)					
CD		Nanodata Corporation, QM-1 Hardware level User's Manual (1975)					
CD		Nanodata Corporation, QM-2 Processor (1976)					
EXAMINER <i>Chamber C Dr</i>				DATE CONSIDERED <i>10/06/03</i>			



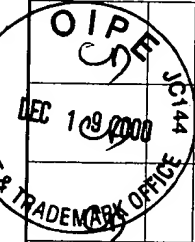
RECEIVED

FORM PTO-1		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		ATTORNEY DOCKET NO. 5231.4-4002	SERIAL NO. 09/348,317
INFORMATION DISCLOSURE CITATION (Use several sheets if necessary)		APPLICANT John S. Yarns, Jr.		FILING DATE July 7, 1999	
		GROUP ART UNIT 2784		Technology Center 2100	
OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Papers, Etc.)					
CD			Robert F. Rosin, Contemporary Concepts of Microprogramming and Emulation, Computing Surveys, Vol. 1 No. 4, pp. 197-212 (December 1969)		
CD			Robert F. Rosin, An Environment for Research in Microprogramming and Emulation, Communications of the ACM, Vol. 15 No. 8, pp. 748-760 (August 1972)		
CD			Standard Computer Corp., IC-9000 Central Processing Unit - Principles of Operation (undated - possibly 1969)		
CD			W.T. Wilner, Design of the Burroughs B1700, Fall Joint Computer Conference, pp. 489-497 (1972)		
CD			W.T. Wilner, Burroughs B1700 Memory Utilization, Fall Joint Computer Conference, pp. 579-586 (1972)		
EXAMINER		DATE CONSIDERED			
Charles C. Du		6/20/03			

FORM PTO-1449		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE			ATTY. DOCKET NO. 30585/4		SERIAL NO. 09/348,317	
INFORMATION DISCLOSURE CITATION (Use several sheets if necessary)					APPLICANT John S. Yates, Jr., et al.			
					FILING DATE July 7, 1999		GROUP ART UNIT 2784	
Group 2700 U.S. PATENT DOCUMENTS								
EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE	
		3,544,969	12/01/70	Rakoczi	340	172.5	Nov. 27, 1967	
		3,781,823	12/25/73	Senese	340	172.5	July 28, 1972	
		4,084,235	04/11/78	Hirtle	364	200	Apr. 14, 1975	
		4,455,602	06/19/84	Baxter	364	200	May 22, 1981	
		4,514,803	4/30/85	Agnew	364	200	Apr. 26, 1982	
		5,638,525	06/10/97	Hammond	395	385	Feb. 10, 1995	
		5,822,578	10/13/98	Frank	395	591	June 5, 1995	
		5,832,205	11/03/98	Kelly	395	185.06	Aug. 20, 1996	
		5,920,721	07/06/99	Hunter	395	705	June 11, 1987	
		5,930,509	07/27/99	Yates	395	707	Jan. 29, 1996	
	5,958,047	09/28/99	Panwar	712	237	Jun. 25, 1997		
FOREIGN PATENT DOCUMENTS								
		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION YES NO	
		WO 96/24895	08/15/96	PCT/US	G06F	9/22		
OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Papers, Etc.)								
CD		Burroughs, Preliminary Edition, B 1700 Systems Reference Manual, Burroughs Corporation (1972)						
CD		Burroughs, B 1700 Systems Micro Implementation Language (MIL) Reference Manual, Burroughs Corporation (1973)						
CD		Anton Chernoff, et al., FX!32, A Profile-Directed Binary Translator, IEEE Micro, Vol. 18 No. 2 pp. 56-64 (March/April, 1998)						
CD		Digital Equipment Corp., White Paper: How DIGITAL FX!32 Works (September 1997) http://www.digital.com/semiconductor/amt/fx32/fx-white.htm						
CD		Ebcioglu and Altman, IBM Research Report, DAISY: Dynamic Compilation for 100% Architectural Compatibility, IBM Research Division (1996)						
CD		Ronald M. Guffin, Microdiagnostics for the Standard Computer MLP-900 Processor, IEEE Transactions on Computers, Vol. C-20 No. 7, pp. 803-808 (July 1971)						
CD		Linley Gwennap, MDR Technical Library Special Report, Intel's Merced and IA-64 Technology and Market Forecast, IA-64 Software Model - Chapter 3 excerpt Microprocessor Report, MicroDesign Resources (July 20, 1998)						
EXAMINER Chamb' C-D					DATE CONSIDERED 10/2/03			
EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.								

FORM PTO-1449		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE			ATTY. DOCKET NO. 30585/4		SERIAL NO. 09/348,317	
INFORMATION DISCLOSURE CITATION (Use several sheets if necessary)					APPLICANT John S. Yates, Jr., et al.			
					FILING DATE July 9, 1999		GROUP ART UNIT 2784	
U.S. PATENT DOCUMENTS								
EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE	
		3,544,969	12/01/1970	Rakoczi	340	172.5	Nov. 27, 1967	
		3,781,823	12/25/1973	Senese	340	172.5	July 28, 1972	
		4,084,235	04/11/1978	Hirtle	364	200	Apr. 14, 1975	
		4,455,602	06/19/1984	Baxter	364	200	May 22, 1981	
		4,514,803	4/30/1985	Agnew	364	200	Apr. 26 1982	
		5,542,059	07/30/1996	Blomgren	395	375	Dec. 21, 1994	
		5,542,109	07/30/1996	Blomgren	395	800	Aug. 31, 1994	
		5,638,525	06/10/1997	Hammond	395	385	Feb. 10, 1995	
		5,822,578	10/13/1998	Frank	395	591	June 5, 1995	
		5,832,205	11/03/1998	Kelly	395	185.06	Aug. 20, 1996	
		5,842,017	11/24/1998	Hookway	395	707	Jan. 29, 1996	
		5,920,721	07/06/1999	Hunter	395	705	Jun. 11, 1997	
		5,930,509	07/27/1999	Yates	395	707	Jan. 29, 1996	
		5,958,047	09/28/1999	Panwar	712	237	Jun. 25, 1997	
		6,000,028	12/07/1999	Chernoff	712	226	Jan. 29, 1996	
	6,031,992	02/29/2000	Cmelik	395	705	Jul. 5, 1996		
	6,091,897	07/18/2000	Yates	395	708	Jan. 29, 1996		
							RECEIVED DEC 27 2000	
FOREIGN PATENT DOCUMENTS								
		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
							YES	NO
		WO 96/24895 Hammond	08/15/1996	PCT/US	G06F	9/22		
OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Papers, Etc.)								
		Burroughs, Preliminary Edition, B 1700 Systems Reference Manual, Burroughs Corporation (1972)						
		Burroughs, B 1700 Systems Micro Implementation Language (MIL) Reference Manual, Burroughs Corporation (1973)						
		Anton Chernoff, et al., FX132, A Profile-Directed Binary Translator, IEEE Micro, Vol. 18 No. 2 pp. 56-64 (March/April, 1998)						
		Compaq Computer Corp., Compiler Writer's Guide for the Alpha 21264, 1999						
EXAMINER <i>Charles C. Dunn</i>				DATE CONSIDERED <i>10/1/03</i>				

Technology Center 2100

FORM PTO-1449		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		ATTY. DOCKET NO. 30585/4	SERIAL NO. 09/348,317
INFORMATION DISCLOSURE CITATION (Use several sheets if necessary)				APPLICANT John S. Yates, Jr., et al.	
				FILING DATE July 9, 1999	
OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Papers, Etc.)					
			Digital Equipment Corp., White Paper: How DIGITAL FX!32 Works (September 1997) http://www.digital.com/semiconductor/amt/fx32/fx-white.htm		
			Ebcioğlu and Altman, IBM Research Report, DAISY: Dynamic Compilation for 100% Architectural Compatibility, IBM Research Division (1996)		
			Ronald M. Guffin, Microdiagnostics for the Standard Computer MLP-900 Processor, IEEE Transactions on Computers, Vol. C-20 No. 7, pp. 803-808 (July 1971)		
	5		Linley Gwennap, MDR Technical Library Special Report, Intel's Merced and IA-64 Technology and Market Forecast, IA-64 Software Model - Chapter 3 excerpt Microprocessor Report, MicroDesign Resources (July 20, 1998)		
	5		Linley Gwennap, MDR Technical Library Special Report, Intel's Merced and IA-64: Technology and Market Forecast, Executive Summary, Microprocessor Report, MicroDesign Resources (July 20, 1998)		
	5		Linley Gwennap, First Merced Patent Surfaces, Intel Document Reveals Processors With Dual Instruction Sets, Microprocessor Report, MicroDesign Resources (July 20, 1998)		
	5		Raymond J. Hookway, et al., Combining Emulation and Binary Translation (August 28, 1997) http://www.digital.com/info/DTJP01HM.HTM		
	5		Intel Corporation, Intel Architecture Software Developer's Manual, <i>passim</i> (1997)		
	5		Harold W. Lawson, Jr., et al., Functional Characteristics of a Multilingual Processor, IEEE Transactions on Computers, Vol. C-20, No. 7, pp. 732-742 (July, 1971)		
	5		Nanodata Corporation, QM-1 Hardware level User's Manual (1975)		
	5		Nanodata Corporation, QM-2 Processor (1976)		
	5		Robert F. Rosin, Contemporary Concepts of Microprogramming and Emulation, Computing Surveys, Vol. 1 No. 4, pp. 197-212 (December 1969)		
	5		Robert F. Rosin, An Environment for Research in Microprogramming and Emulation, Communications of the ACM, Vol. 15 No. 8, pp. 748-760 (August 1972)		
	5		Standard Computer Corp., IC-9000 Central Processing Unit - Principles of Operation (undated - possibly 1969)		
	5		W.T. Wilner, Design of the Burroughs B1700, Fall Joint Computer Conference, pp. 489-497 (1972)		
5		W.T. Wilner, Burroughs B1700 Memory Utilization, Fall Joint Computer Conference, pp. 579-586 (1972)			
				RECEIVED	
EXAMINER <i>Chauh. C. Dm</i>				DATE CONSIDERED <i>10/10/99</i>	
				DEC 27 2000 Technology Center 2100	